- [CPU] Explain? List components
- [Register]: Define? Types?
- [Instructions]: Define? Types? Context? How is instructions executed in a sequence (6 steps)?
- [Aim] of cpu designers to produce a perfect computer
    - [Aim1]: Explain
    - [Aim 2]: Explain? How is it done
    - [Aim 3]: Explain
    - [Aim 4]: Explain
    - [Aim 5]: Explain
- [Aim 5]: List the different types (4)

- [Microarchitecture]: Explain
- [Operands]: Explain
- [Adressng modes]: Explain? Types

- [Instruction Set Architecture]: Explain? Properties of good/bad ISA:
- [ISA]: Properties that belong/influence the ISA and don't belong